

 Repondre  Repondre c tous  Transférer    Fermer  Aide

De: M.Therese Capron Date: mer. 05/05/2010 23:14
Bertrand Clou ; Vincent Teil; Franck Da Costa ; Hassan Chaoui ; Henrique Fernandes ; Olivier Causse ; Paolo Migliavacca ; Stephanie Cambounet ; Patrick Gueulle ; William Clemens ; Bruno Beyron; Alexandre Apcher ;
?: Anissa Karray-Kbaier ; Julien Clemens ; Frederic Garcia ; Steve Charpentier ; Geraldine Saint Upery ; Christophe Salamero ; Remi Gerber ; Loic Cunnac ; Remy Saphon; Jose Capilla ; Laurent Bordes ; Patrice Delpy ; Virginie Duchemann; Damien Bruvier ; Samir Dkhissi ; Cor Voorwinden
Cc: Dominique Omet; Berengere Le Men; Dominique Romeo; Stephane Colomines ; Stephane Ramond; Christophe Pinatel ; Pascal Tournier ; Marc Dos Santos ; Marc Ryat ; Pierre Genest
Objet : RE: NCP6922, NCP6914 PMICs & NCP63xy Stdalone DCDC Tape Out
PiDces jointes :

[Afficher sous forme de page Web](#)

Un grand merci c tous pour ces 3 tape out, votre travail et contribution de tous ces derniers mois : cœest un grand milestone pour LVPM et les sous-systDmes comme les mini PMIC ont beaucoup de visibilitc (visite de Keith le 25 mai) et dœintcrdt auprDs de nos vende urs . Les ctages suivants de la fusce vont vite se remplir n.

Le retour du silicium et de leurs bons resultats sera c fdter autour dœun repas

Amitics

Marie

From: Bertrand Clou

Sent: Wednesday, May 05, 2010 15:14

To: Vincent Teil; Franck Da Costa; Hassan Chaoui; Henrique Fernandes; Olivier Causse; Paolo Migliavacca; Stephanie Cambounet; Patrick Gueulle; William Clemens; Bruno Beyron; Alexandre Apcher; Anissa Karray-Kbaier; Julien Clemens; Frederic Garcia; Steve Charpentier; Geraldine Saint Upery; Christophe Salamero; Remi Gerber; Loic Cunnac; Remy Saphon; Jose Capilla; Laurent Bordes; Patrice Delpy; Virginie Duchemann; Damien Bruvier; Samir Dkhissi; Cor Voorwinden

Cc: Dominique Omet; M.Therese Capron; Berengere Le Men; Dominique Romeo; Stephane Colomines; Stephane Ramond; Christophe Pinatel; Pascal Tournier; Marc Dos Santos; Marc Ryat; Pierre Genest

Subject: NCP6922, NCP6914 PMICs & NCP63xy Stdalone DCDC TapeOut

Vincent, Alexandre, Anissa, et Julien font partir cette aprDs midi nos 3 petits powe r management

Certes ce nœest pas la fusce Ariane, mais cœest le premier gros ctage de lœcdifice mini PMIC

Donc Je vous invite c nous retrouver c 16h30 c la pause cafc, pour partager une peti te coupe de champagne, afin de cœlcbrrer ce tape out qui est hyper important pour lœcquipe.

Merci en tout cas c tous pour tout le boulot accompli depuis en particulier ces six derniers mois.

PS : si jamais jœai oublie par malheur qqn nœhsitez pas c lœinviter en esperant que chacun aura droit a sa goutte de champagne J

Bertrand

From: Bertrand Clou

Sent: Monday, May 03, 2010 8:50 AM

To: Vincent Teil; Franck Da Costa; Hassan Chaoui; Henrique Fernandes; Olivier Causse; Paolo Migliavacca; Stephanie Cambounet; Patrick Gueulle; William Clemens; Bruno Beyron; Alexandre Apcher; Anissa Karray-Kbaier; Julien Clemens; Frederic Garcia; Steve Charpentier

Subject: RE: Minutes technical status of PMICs and Stalone DCDC projects

Summary of our last Thursday meeting:

There are few critical actions to close before tape out of the 3 products

1/ top cell simulations:

- Full transistor level simulation of the NCP6922 IC (using floating node method) to check off mode consumption (Hassan/Stephanie)
- Check functionality of the I2C bit which is powered by AVIN and de-activate I2C functionality while HWEN=0 (Hassan/Stephanie)
- OTP simulations (emulations, programmability) (Frederic)

2/ block simulations

- Buck perturbation in standalone configuration with high current: (Henrique, Olivier)
- o Check PCB and package parasitic: looks to be over estimate
- o Check solution to reduce ground perturbations on the feedback signal
- Band-gap start-up and its power transition from Avin to Vcore
- o Band-gap simulation at Vcore=1.6V (Paolo, Vincent)
- o Check band-gap voltage drop at its input supply transition (when Vcore is ready) (Bertrand, Vincent)

3/ Layout

- Check to adapt a true pull-down on the NCP63xy (activated while HWEN=0) with a simple metal option (Vincent/Anissa)
- Final check with FCI of the NCP6922 & NCP6914 RDL feasibility (Alexandre/William)
- Need to prepare a DRC waiver justification file for the low voltage ESD protection (Alexandre)

Tape out of the base layers (before contact) of NCP6922, NCP6914 and NCP63xy is planned for next Wednesday

This will let about 3 weeks to complete some less critical top level simulations.

I will set a meeting on Tuesday to review the progress on these critical open points .

Bertrand

From: Vincent Teil

Sent: Thursday, April 29, 2010 7:40 PM

To: Franck Da Costa; Hassan Chaoui; Henrique Fernandes; Bertrand.Clou; Olivier Causse; Paolo Migliavacca; Stephanie Cambounet; Vincent Teil; Patrick Gueulle; William Clemens; Bruno Beyron; Alexandre Apcher; Anissa Karray-Kbaier; Julien Clemens; Frederic Garcia; Steve Charpentier

Subject: Minutes technical status of PMICs and Stdalone DCDC projects