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De: William Clemens Date: ven. 08/07/2011 15:20
?: Berengere Le Men ; Alexandre Apcher ; Virginie Duchemann; Fabien Foulon; Patrice Delpy
Cc:
Objet : RE: first evaluation results of the NCS8381
PiDces jointes :

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Tout est dit n Excellent !! Le weekend sœannonce bien J

William Clemens | Project Manager

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From: Berengere Le Men
Sent: Friday, July 08, 2011 2:44 PM
To: Alexandre Apcher; Virginie Duchemann; Fabien Foulon; William Clemens; Patrice Delpy
Subject: FW: first evaluation results of the NCS8381

Pour le plaisir J

From: Eric Joseph
Sent: Thursday, July 07, 2011 7:21 PM
To: Berengere Le Men
Cc: Bertrand Clou
Subject: RE: first evaluation results of the NCS8381

Great job Berengere. Please pass on my congratulations to the team.

Eric Joseph

VP, Design Engineering, CCPG

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1-602-451-6110

From: Berengere Le Men

Sent: Wednesday, July 06, 2011 7:59 AM

To: Thibault Kassir; Eric Joseph

Cc: Stephen Dow; Radhika Venkatasubramanian; Adrian Lin; Brian Ballweber; David Lovelace; Martin Kejhar; Ling-Miao Chou; Josh Warner; Darren Hooks; Lang Dinh; Bob Boos; David McDonald; Bertrand Clou; Tim Gurnett; Philippe Andre; Dominique Romeo

Subject: first evaluation results of the NCS8381

Hello Thibault and Eric,

We just received the NCS8381 samples and we would like to share with you good news.

Indeed the preliminary evaluation shows good operating of the device. We did some audio listening of the amplifier in different modes and different power supplies with good responses at low and high power (full scale range) and in stereo. We did also some preliminary measurements on a first circuit with THD results in line with what expected (0.02% at 1kHz 8W 8Ohms and 16V). Noise measurements show results for now a little higher than expected (SNR=97dB instead of 100dB) but it's still very preliminary and investigation is ongoing. We tested also quickly functions like volume control, balance left and right, anti-clipping and DRC functions with good results. We tested different sample rates and different configurations which look ok.

The first results validate that the following blocks are functional:

- I2C/I2S interfaces
- Power tree (bias, 1.875V regulator and low noise buffer)
- 400MHz PLL
- Digital Signal Processing: Interpolation filters, PCM-PWM Modulator

- Digital audio enhancement: DRC, PGA, Balance, anti-clip
- PWM analog amplifier

These results are preliminary and need of course to be refined: the evaluation is pu rsuing. We keep you posted.

If you want to enjoy the real life demo (audio), we will make it available soon unde r sharepoint project folder.

Best regards,

Berengere LE MEN, Philippe ANDRE, Dominique ROMEO