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De: M.ThereSe Capron Date: lun. 14/03/2011 19:20
 ?: Laurent Bordes ; Myriam Combes ; Alexandre Apcher ; Patrice Delpy ; Maxime Lybliamay ; Pascal Sevetian ; Crystal Zins-Lam ; Bernard Remaury
 Cc: Bertrand Clou
 Objet : RE:
 PiDces jointes :

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Laurent

Super: merci beaucoup c toute lœequipe . reste c corriger notre court circuit

Amities

Marie

From: Laurent Bordes
Sent: Monday, March 14, 2011 19:16
To: Myriam Combes; Alexandre Apcher; Patrice Delpy; Maxime Lybliamay; Pascal Sevetian; Crystal Zins-Lam; Bernard Remaury
Cc: Bertrand Clou; M.ThereSe Capron
Subject:

Hi All,

Please find in the below table the traceability matrix on the NCP391FCAL product pas s 1p0 at room temperature.

Main changes on the NCP391 are the Power FET integration which cannot be evaluated due to the short and the design changes on the UVLO, OVLO and TSD parameters. These last can be evaluated despite the short thanks to the FLAG behavior.

Many thanks to Maxime and Pascal for their measurements. All their data can be seen in the enclosed file for deeper analysis.

I noticed the characterization measurements give UVLO and OVLO threshold parameters decreasing versus temperature which was not the case in my simulations.

Nevertheless, the parameters are in the specification range over temperature sweep with comfortable margins.

Feel free to comment and do not hesitate to ask for further questions.

Regards,

Laurent

Parameter	NCP391FCAL Product - Pass 1p0			Specification		Design		Eval/Charac		Test
	Symbol	Conditions	Min	Typ	Max	Target	Measure	Measure	Units	
Undervoltage Lockout Threshold	UVLO	Vin falls below UVLO threshold from 5 to 2.7V	2.8	2.95	3.1	2.952	2.952	2.952	V	
Undervoltage Lockout Hysteresis	UVLOhyst	Vin rises above UVLO + UVLOhyst	30	60	90	60	51	59.5	mV	
Overvoltage Lockout Threshold	OVLO	Vin rises above OVLO threshold	7.16	7.4	7.65	7.428	7.387	7.456	V	

Overvoltage Lockout Hysteresis	OVLOhyst	Vin falls below OVLO + OVLOhyst	50	100	150	115	101	115	mV
Thermal Shutdown Temperature	Tsd			150		152	149	N.A.	GC
Thermal Shutdown Hysteresis	Tsd_hyst			15		24	27	N.A.	GC
Supply Quiescent Current	IDD	No Load Enb=GND		140	250			105.2	uA