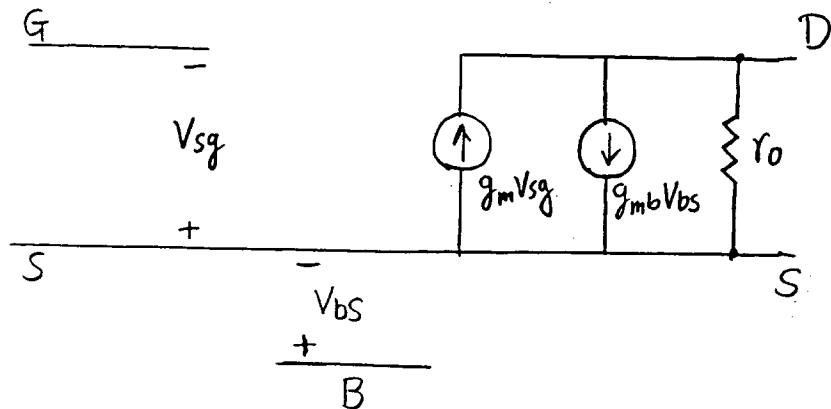


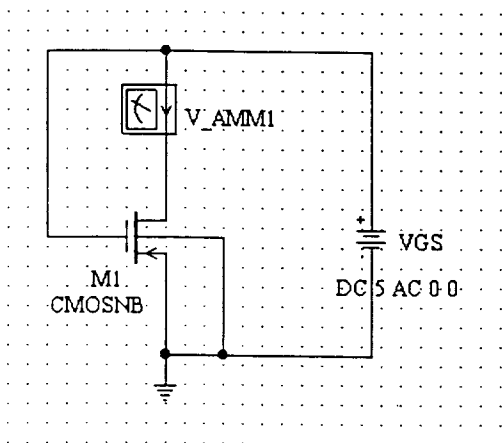
# Chapter 9

## Problem 9.1

The small signal model of P-channel MOSFET is shown below:



## Problem 9.2



\*\*\* Top Level Netlist \*\*\*

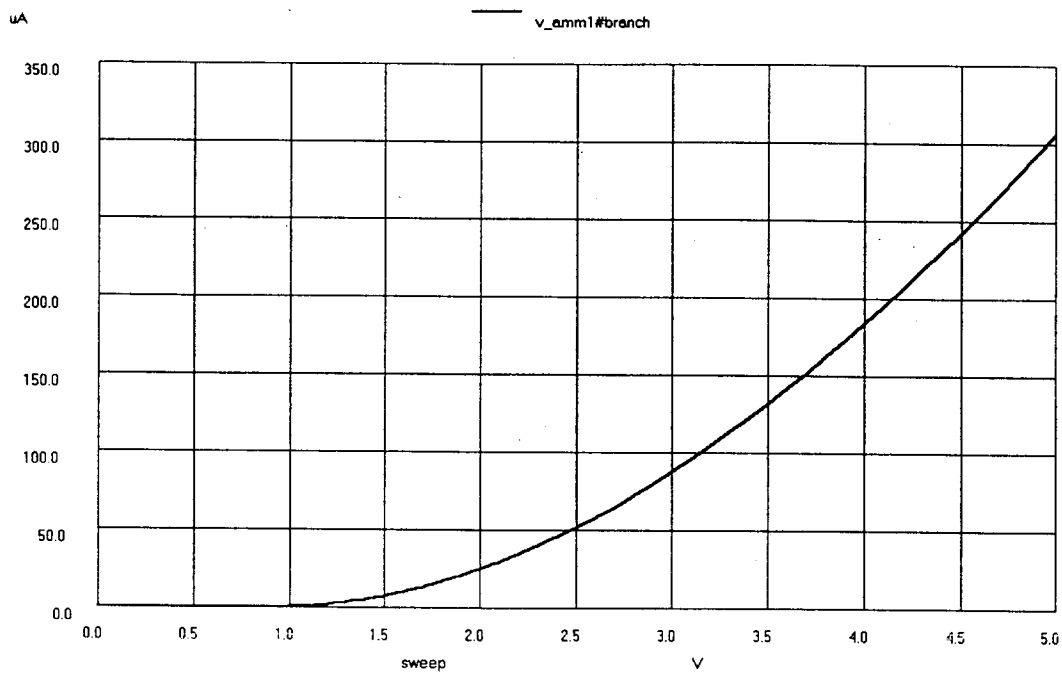
```
M1 9 1 0 0 CMOSNB L=5u W=5u
V_AMM1 1 9 0 V
VGS 1 0 DC 5 AC 0 0
```

\*\*\*\*\* Spice models and macro models \*\*\*\*\*

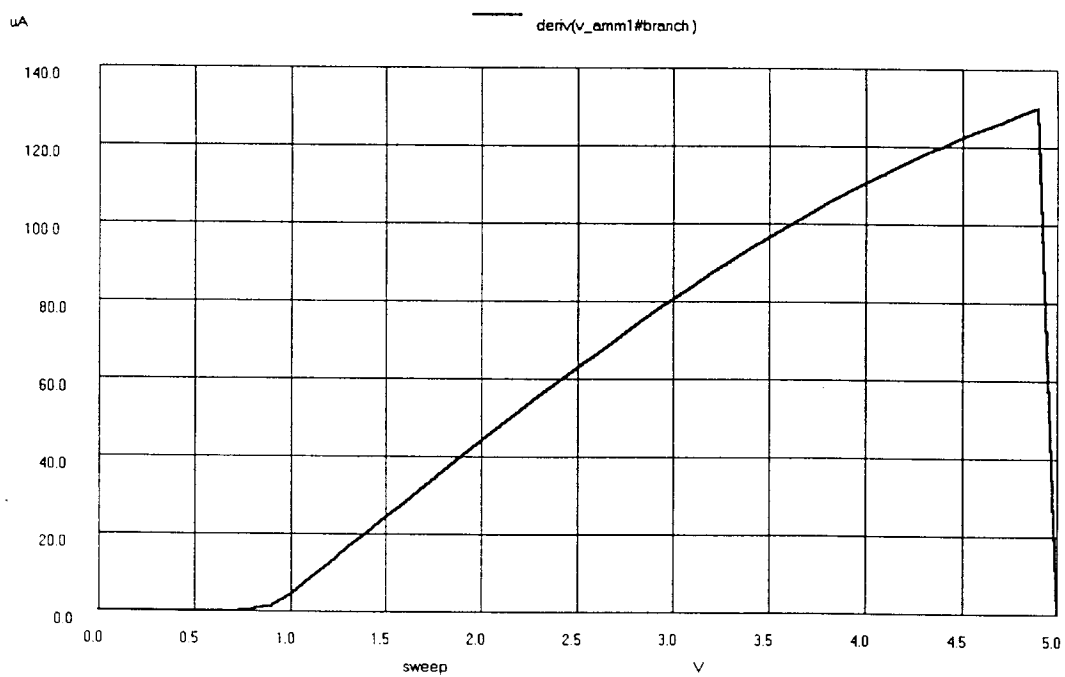
```
.MODEL CMOSNB NMOS LEVEL=4
+VFB=-9.73820E-01, LVFB=3.67458E-01, WVFB=-4.72340E-02
...
+pbsw=0.8 mj=0.66036 mjsw=0.178543 wdf=0 dell=0
```

\*\*\*\*\* End of spice models and macro models \*\*\*\*\*

```
.DC VGS 0 5 0.1
.end
```



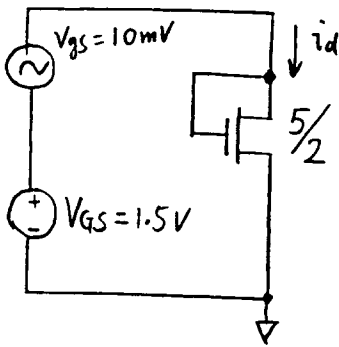
**Fig 9.2(a) Drain current vs  $V_{gs}$**



**Fig 9.2(b) Derivative of curve in Fig 9.2(a)**

The line tangent at  $V_{gs} = 2V$  is about  $44 \mu A/V$ . The reciprocal of this slope corresponds to the resistance of the MOSFET in diode connection.

# Problem 9.3

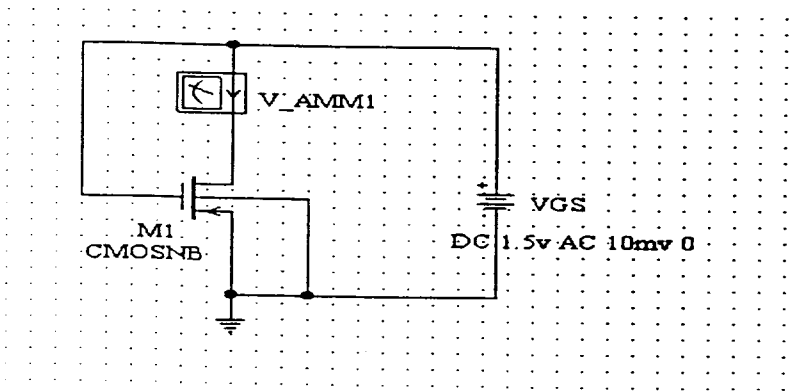


MOSFET operates in saturation region.  $V_{THN} = 0.83V$

$$g_m = \beta (V_{GS} - V_{THN}) = k_{PN} \times \frac{W}{L} (V_{GS} - V_{THN})$$

$$= 50 \mu A/V^2 \times \frac{5 \mu m}{2 \mu m} \times (1.5V - 0.83V) = 83.75 \mu A/V$$

$$i_d = g_m V_{gs} = 83.75 \mu A/V \times 0.01V \approx \underline{0.838 \mu A}$$



\*\*\* (TurboSim V 1.87) Netlist for C:\TURBOSIM\EE445\p91.CKT

\*\*\* Top Level Netlist \*\*\*

```
M1      3 1 0 0 CMOSNB L=2u W=5u
V_AMM1  1 3 0V
VGS     1 0    DC 1.5v AC 10mv 0
```

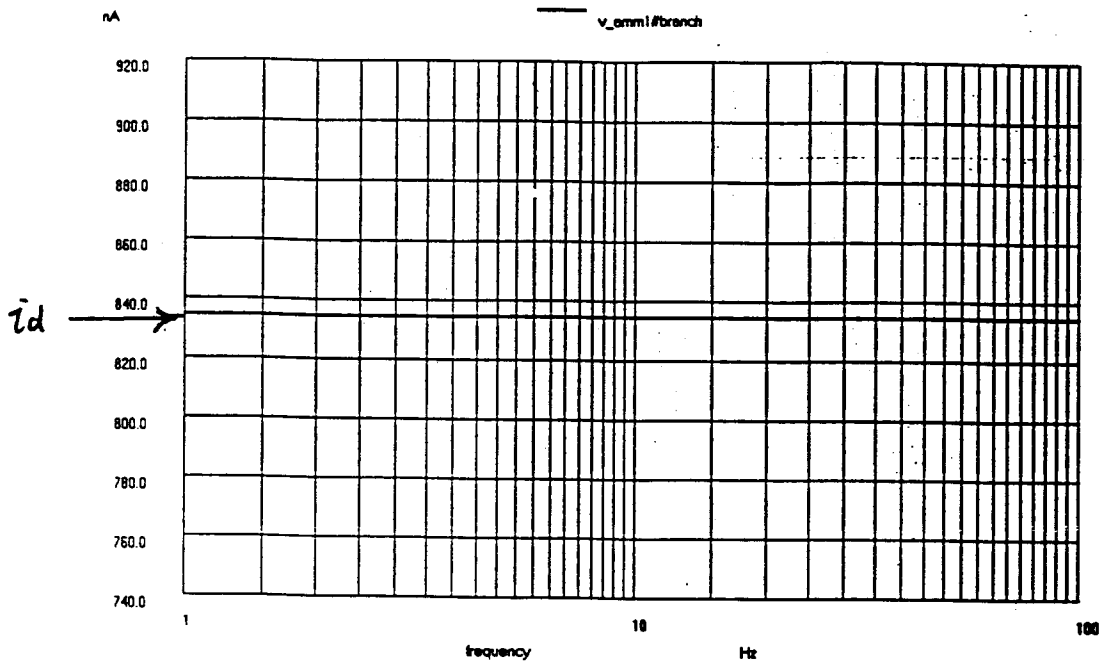
\*\*\*\*\* Spice models and macro models \*\*\*\*\*

```
.MODEL CMOSNB NMOS LEVEL=4
+VFB=-9.73820E-01, LVFB=3.67458E-01, WVFB=-4.72340E-02
...
+pbsw=0.8  mj=0.66036  mjsw=0.178543  wdf=0  dell=0
```

\*\*\*\*\* End of spice models and macro models \*\*\*\*\*

```
.ac LIN 100 1 100
.end
```

The AC SPICE simulation result is shown below:



#### Problem 9.4

1. Method one:

$$\text{Generally, } i_d = \frac{\bar{a}_D}{\bar{a}_{DS}} v_{ds}.$$

$$\frac{\bar{a}_D}{\bar{a}_{DS}} = (\beta_n/2) \cdot (V_{GS} - V_{thn})^2 \cdot \lambda = (50/2) \cdot (15/5) \cdot (1.5 - 0.83) \cdot 0.06 = 2.02 \text{ nA/V}.$$

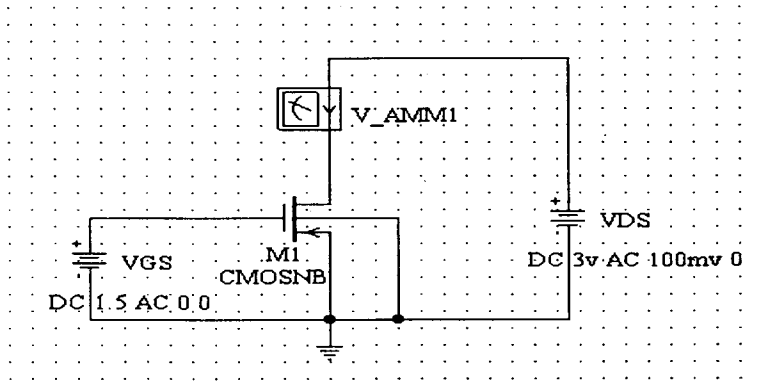
$$i_d = 2.02 \text{ nA/V} \cdot 100 \text{ mV} = 202 \text{ nA}.$$

2. Method Two-small signal model.

$$I_D = (\beta_n/2) \cdot (V_{GS} - V_{thn})^2 \text{ and } r_0 = 1/(\lambda \cdot I_D). \text{ Plug into the values } \implies r_0 = (1/2.02) \text{ V/uA}.$$

$$i_d = v_{ds}/r_0 = 100 \text{ mV} / [(1/2.02) \text{ V/uA}] = 202 \text{ nA}.$$

### Problem 9.5



\*\*\* Top Level Netlist \*\*\*

```
M1      3 6 0 0 CMOSNB L=5u W=15u
V_AMM1      1 3 0V
VDS      1 0      DC 3v AC 100mv 0
VGS      6 0      DC 1.5 AC 0 0
```

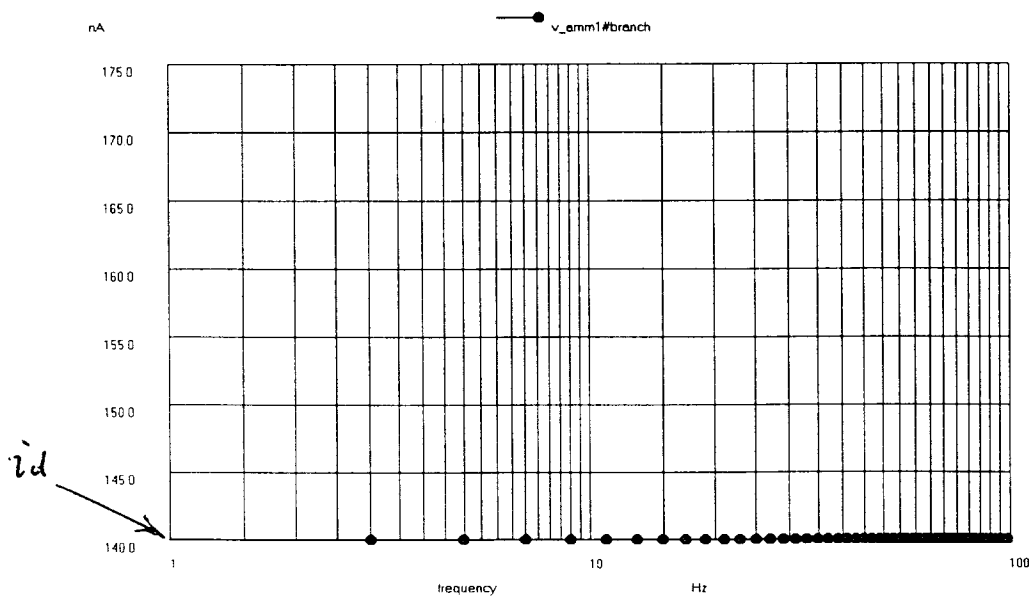
\*\*\*\*\* Spice models and macro models \*\*\*\*\*

```
.MODEL CMOSNB NMOS LEVEL=4
+VFB=-9.73820E-01, LVFB=3.67458E-01, WVFB=-4.72340E-02
... ..
+pbsw=0.8  mjs=0.66036  mjsw=0.178543  wdf=0  dell=0
```

\*\*\*\*\* End of spice models and macro models \*\*\*\*\*

```
.ac LIN 100 1 100
.end
```

The AC SPICE simulation result is shown below:



### Problem 9.6

$$\because V_{DS} = 1.5\text{V} < V_{GS} - V_{THN} = 3\text{V} - 0.83\text{V} = 2.17\text{V}$$

$\therefore$  MOSFET operates in triode region

$$R_{ch} = \frac{1}{\beta(V_{GS} - V_{THN}) - \beta V_{DS}} = \frac{1}{50\mu\text{A/V}^2 \times \frac{15\mu\text{m}}{5\mu\text{m}} \times (3\text{V} - 0.83\text{V} - 1.5\text{V})}$$

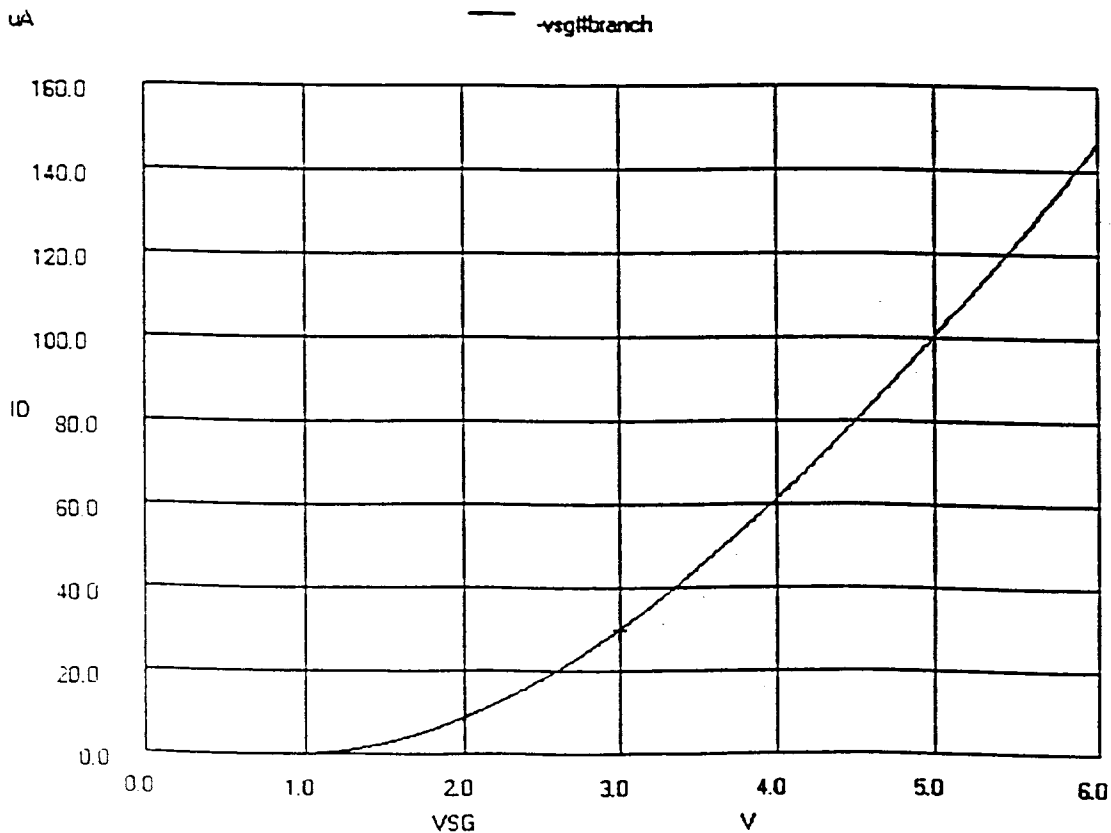
$$\approx 9.95\text{K}$$

$$i_d = \frac{V_{DS}}{R_{ch}} = \frac{100\text{mV}}{9.95\text{K}} \approx \underline{\underline{10.1\mu\text{A}}}$$

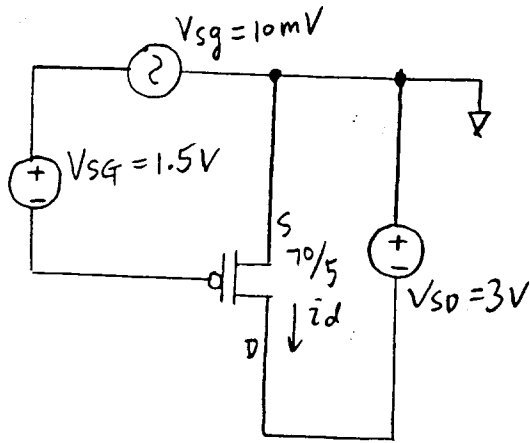
$$i_d = v_{ds}/r_0 = 100\text{mV}/[(1/2.02)\text{V}/\mu\text{A}] = 202\text{ nA.}$$

### Problem 9.7

The plot is shown below.



### Problem 9.8



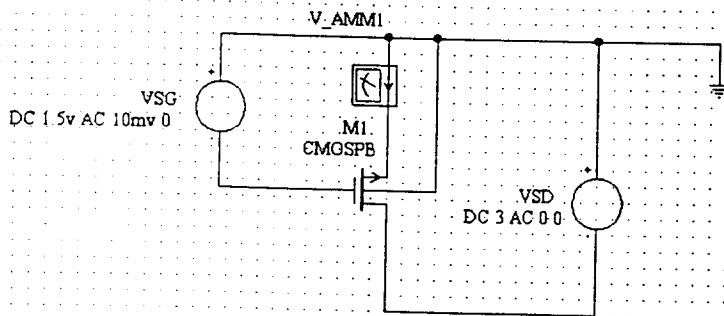
Since the source is common, its potential is irrelevant.

$$g_m = \beta(V_{SG} - V_{THP}) = K_{PP} \times \frac{W}{L} \times (V_{SG} - V_{THP}) = \frac{17\mu A}{V^2} \times \frac{70\mu m}{5\mu m} \times (1.5V - 0.91V)$$

$$\approx 140.4 \mu A/V$$

$$i_d = g_m V_{sg} = 140.4 \mu A/V \times 0.01V \approx \underline{1.4 \mu A}$$

### Problem 9.9



\*\*\* Top Level Netlist \*\*\*

```
M1 1 2 3 0 CMOSP L=5u W=70u
V_AMM1 0 3 0V
VSD 0 1 DC 3 AC 0 0
VSG 0 2 DC 1.5v AC 10mv 0
```

\*\*\*\*\* Spice models and macro models \*\*\*\*\*

```
.MODEL CMOSP PMOS LEVEL=4
```

```
+ vfb=-2.65334E-01, lvfb=6.50066E-02, wvfb=1.48093E-01
```

```
... ..
```

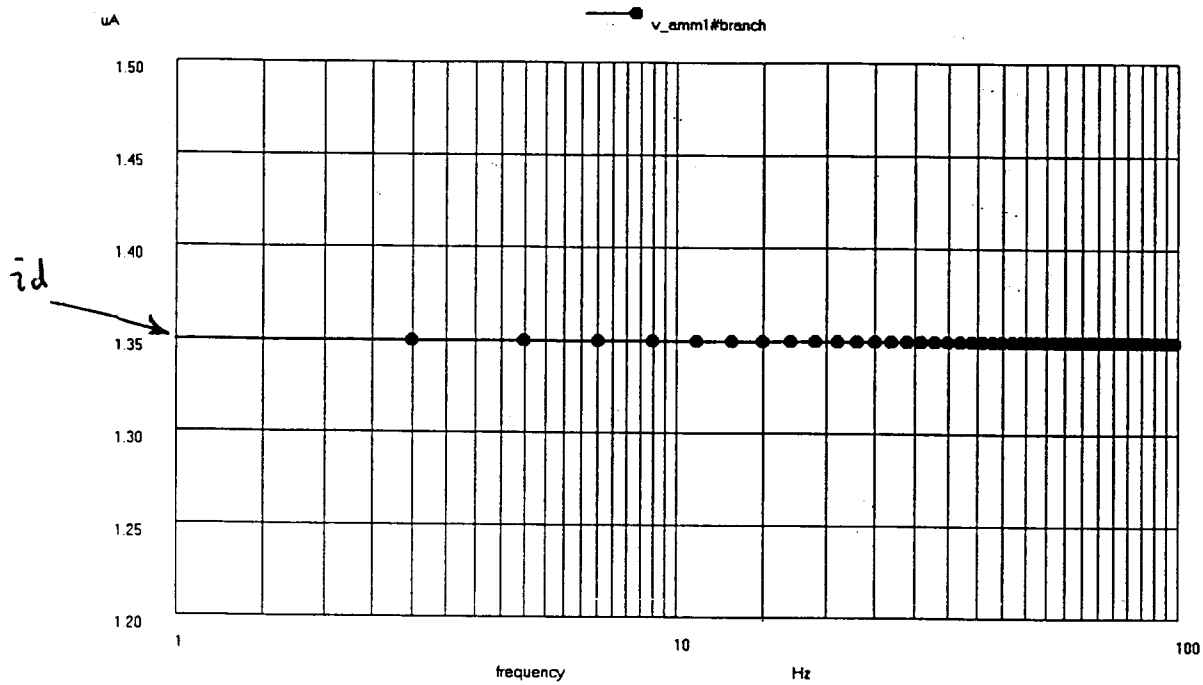
```
+ pbsw=0.8, mjs=0.60438, mjsw=0.244194, wdf=0, dell=0
```

\*\*\*\*\* End of spice models and macro models \*\*\*\*\*

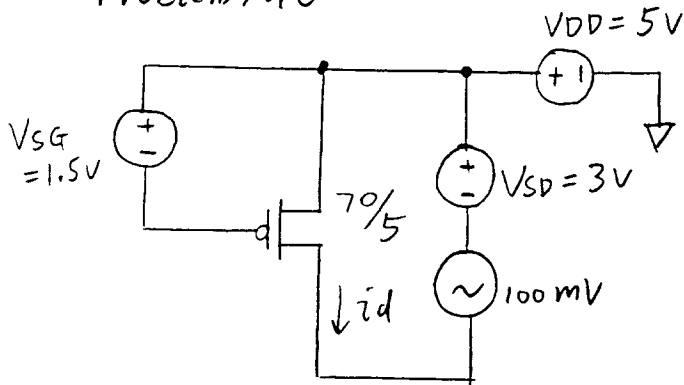
```
.ac LIN 100 1 100
```

```
.end
```

The AC SPICE simulation result is shown below:



Problem 9.10



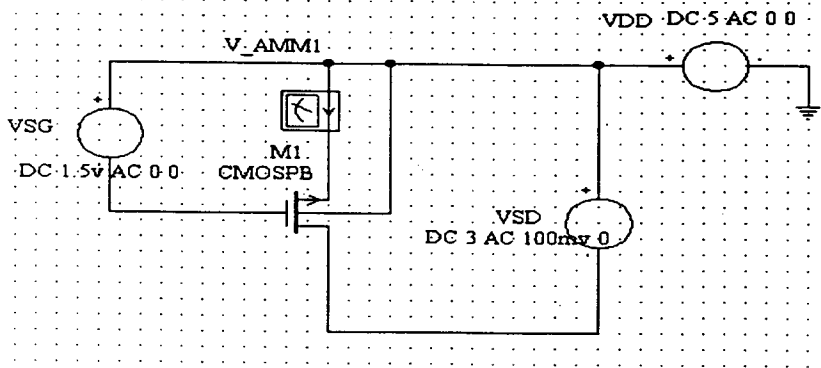
MOSFET operates in Saturation region.

$$I_D = \frac{17\mu A}{V^2} \times \frac{1}{2} \times \frac{70\mu m}{5\mu m} \times (1.5V - 0.91V)^2 \approx 41.4\mu A$$

$$r_o = \frac{1}{I_D \lambda} = \frac{1}{41.4\mu A \times 0.06V^{-1}} = 0.402M = 402k\Omega$$

$$i_d = \frac{V_{sd}}{r_o} = \frac{100mV}{402k} \approx \underline{0.25\mu A}$$

**Problem 9.10 (Cont.)**



\*\*\* Top Level Netlist \*\*\*

```
M1      1 2 3 5 CMOSPB L=5u W=70u
V_AMM1      5 3 0V
VDD      5 0      DC 5 AC 0 0
VSD      5 1      DC 3 AC 100mv 0
VSG      5 2      DC 1.5v AC 0 0
```

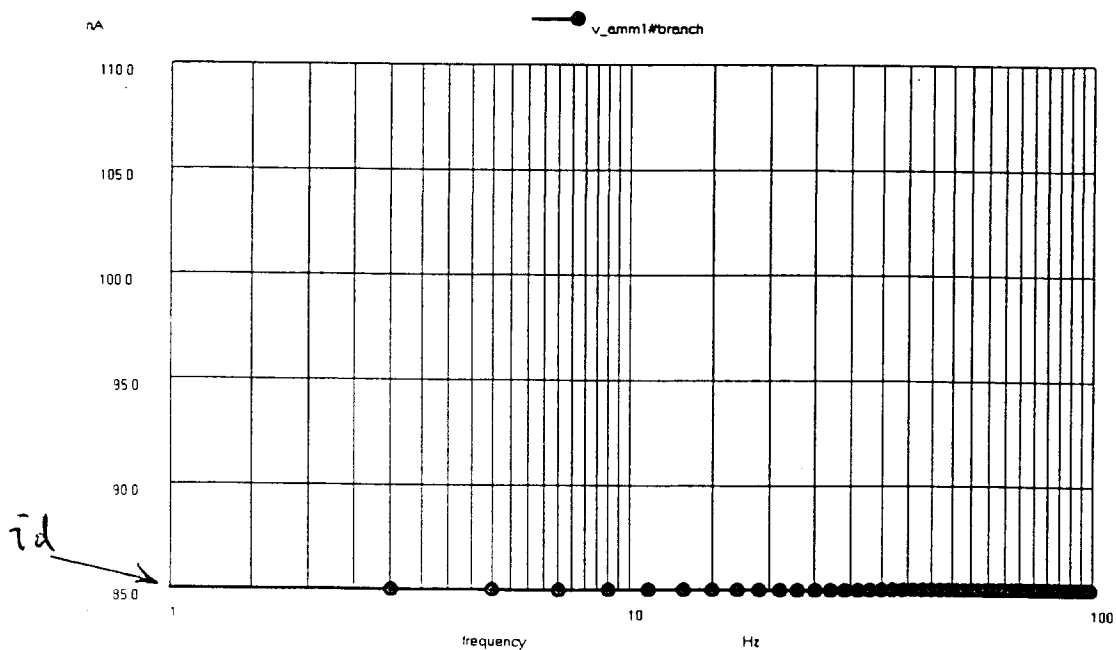
\*\*\*\*\* Spice models and macro models \*\*\*\*\*

```
.MODEL CMOSPB PMOS LEVEL=4
+ vfb=-2.65334E-01, lvfb=6.50066E-02, wvfb=1.48093E-01
... ..
+ pbsw=0.8, mj=0.60438, mjsw=0.244194, wdf=0, dell=0
```

\*\*\*\*\* End of spice models and macro models \*\*\*\*\*

```
.ac LIN 100 1 100
.end
```

The AC SPICE simulation result is shown below:



Problem 9.11

ANS: The size of the drain and source regions is  $6\mu\text{m} \times 14\mu\text{m}$ . Assume  $V_{DB} = V_{SB} = 0$ .

$$C_{sb} = C_{db} = C_J \cdot A_D + C_{JSW} \cdot P_D = 3.25 \times 10^{-4} \text{ F/m}^2 \times 6\mu\text{m} \times 14\mu\text{m} + 2.54 \times 10^{-10} \text{ F/m} \times 2(14+6)\mu\text{m} = 27.3 \text{ f} + 10.16 \text{ f} = 37.46 \text{ fF}.$$

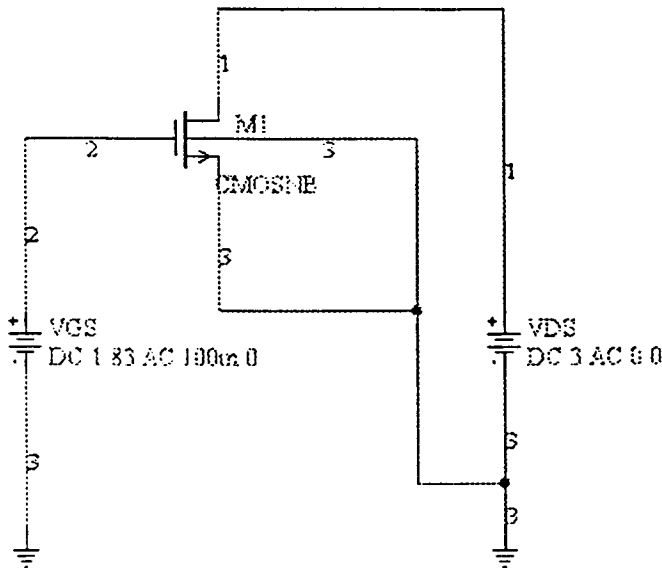
$$C_{gb} = C_{GBO} \cdot L = 3.85 \times 10^{-10} \text{ F/m} \cdot 10\mu\text{m} = 3.85 \text{ fF}.$$

$$\text{In triode region: } C_{gs} = C_{gd} = (1/2) C_{ox}' W L = 0.5 \times (800 \text{ aF}/\mu\text{m}^2) \times 14\mu\text{m} \times 10\mu\text{m} = 56 \text{ fF}.$$

$$\text{In saturation region: } C_{gs} = (2/3) C_{ox}' W L = 74.67 \text{ fF}. \quad C_{gd} = C_{GDO} \cdot W = 5.03 \times 10^{-10} \text{ F/m} \cdot 14\mu\text{m} = 7.04 \text{ fF}.$$

Problem 9.12

ANS: The circuit, netlist and plots are shown in the following figure and  $V_{GS} = 1.83\text{v}$ . For n-channel, SPICE simulation gives  $f_T = 2.2\text{ghz}$ . Notice it is depend on the  $V_{GS}$  value. At  $f_T |i_d/i_g| = 1$  and at DC  $|i_d/i_g| = \infty$ .

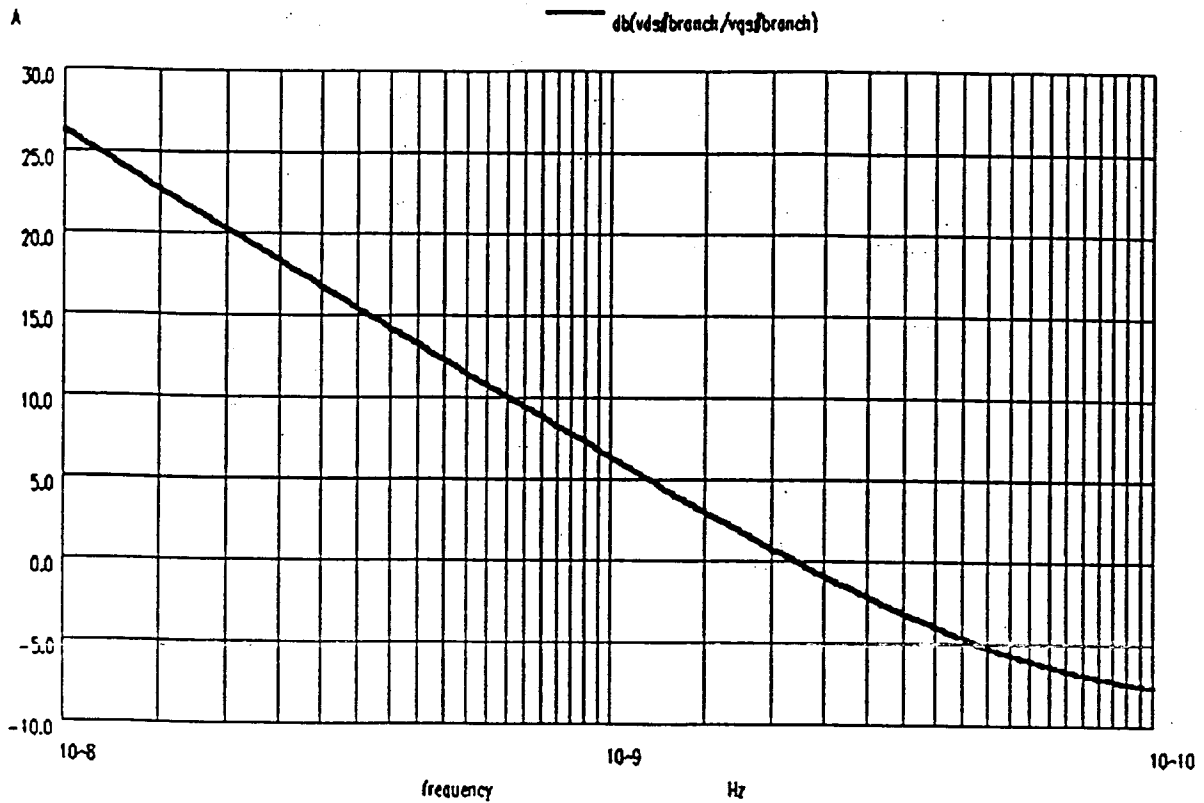


The netlist is as following:

```

*** (TurboSim V 1.1) Netlist for C:\TSP912.CKT
*** Top Level Netlist ***
M1      1 2 0 0 CMOSNB L=2u W=3u
VDS     1 0    DC 3 AC 0 0
VGS     2 0    DC 1.83 AC 100m 0
***** Spice models and macro models *****
.MODEL CMOSNB NMOS LEVEL=4
**BSIM Model Parameters**
***** End of spice models and macro models *****
.ac DEC 50 100MEGhz 100ghz
.end
    
```

N-channel SPICE simulation plot:



Problem 9.13

The unity current gain frequency,  $f_T$ , of a MOSFET, is given by

$$f_T = \frac{K_p \times W (V_{GS} - V_{THN})}{2\pi \times L \times (C_{gb} + C_{gs} + C_{gd})}$$

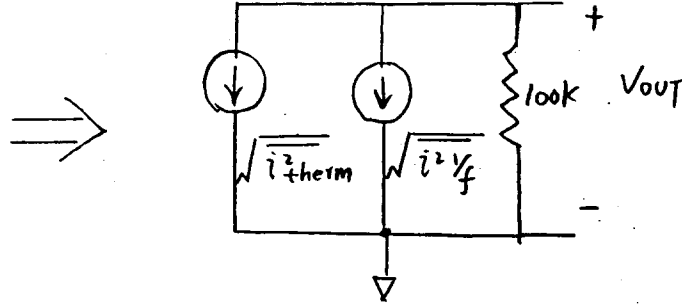
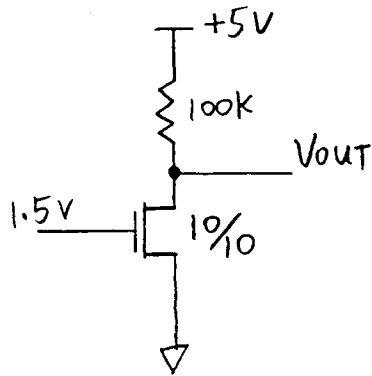
If  $V_{GS} - V_{THN}$  is very small, the speed of the circuit will be low.

Problem 9.14

ANS: a) With  $V_{GS}$  around  $V_{thn}$ , as we can see from figure 9.10, that  $V_{thn}$  decreases with increasing temperature. With  $V_{GS} \gg V_{thn}$ , as we can from figure 9.11, that  $V_{thn}$  will increase as the temperature goes up.

b) For the case  $V_{thn}$  around a few hundred millivolts, if the temperature goes up, since  $V_{thn}$  will decrease, the MOSFET designed to be cutoff may turn on because  $V_{thn}$  decreases and the MOSFET designed to be in saturation  $V_{DS} > V_{GS} - V_{thn}$  may get into the triode region as  $V_{DS} < V_{GS} - V_{thn}$  as  $V_{thn}$  decrease. In addition, if we design to let MOSFETs operating in subthreshold region for low power consideration, since the drain current is exponentially related to the  $V_{GS} - V_{thn}$ , any changes in  $V_{thn}$  can cause significant changes in the drain current and mismatch problems.

# Problem 9.16



$$I_D = \frac{k_{PN}}{2} \frac{W}{L} (V_{GS} - V_{THM})^2 = \frac{50 \mu A/V^2}{2} \times \frac{10 \mu m}{10 \mu m} \times (1.5V - 0.83V)^2 \approx 11.2 \mu A$$

$$\sqrt{i_{therm}^2} = \sqrt{6KT \sqrt{2\beta \cdot I_D}} = \sqrt{6 \times 1.38 \times 10^{-23} \times 300 \times \sqrt{2 \times 50 \mu A/V^2 \times \frac{10}{10} \times 11.2 \mu A}}$$

$$\approx 0.91 \text{ pA}/\sqrt{\text{Hz}}$$

$$\sqrt{V_{therm}^2} = 0.91 \text{ pA}/\sqrt{\text{Hz}} \times 100 \text{ k} = 91 \text{ nV}/\sqrt{\text{Hz}}$$

$$\sqrt{i_{1/f}^2} = \sqrt{\frac{KF \times I_D^{AF}}{f \cdot C_{ox}' \times L^2}} = \sqrt{\frac{10^{-25} \times (11.2 \times 10^{-6} \text{ A})^{1.3}}{f \times 800 \text{ aF}/\mu m^2 \times (10 \mu m)^2 \times 10^{-18}}}$$

$$\approx 0.677 \text{ nA}/\sqrt{\text{f}}$$

$$\sqrt{V_{1/f}^2} = 0.677 \text{ nA}/\sqrt{\text{f}} \times 100 \text{ k} = 67.7 \mu V/\sqrt{\text{f}}$$

$$\sqrt{i_{100k}^2} = \sqrt{\frac{4KT}{R}} = \sqrt{\frac{4 \times 1.38 \times 10^{-23} \times 300}{100 \times 10^3}} = 0.41 \text{ pA}/\sqrt{\text{Hz}}$$

$$\sqrt{V_{100k}^2} = 0.41 \text{ pA}/\sqrt{\text{Hz}} \times 100 \text{ k} = 41 \text{ nV}/\sqrt{\text{Hz}}$$

Problem 9.16 (cont.)

$$\overline{v_{on}^2} = \int_1^{1000} \left[ (91 \times 10^{-9})^2 + (41 \times 10^{-9})^2 + (67.7 \times 10^{-6})^2 \cdot \frac{1}{f} \right] df$$

$$\approx 3.2 \times 10^{-8} \text{ V}^2$$

$$\therefore \sqrt{\overline{v_{on}^2}} = 1.79 \times 10^{-4} \text{ V} = \underline{\underline{179 \mu\text{V}}}$$